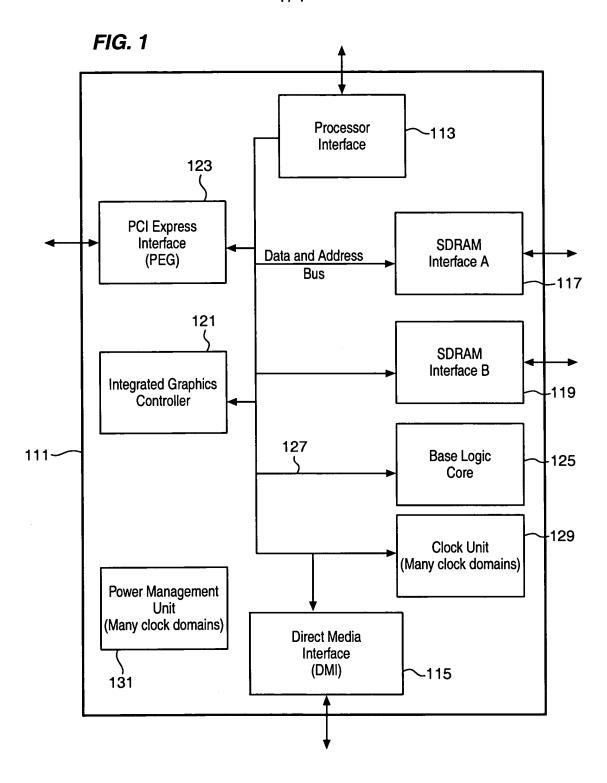
Title: "Optimized Memory Addressing"
Inventors: David E. Frecker Filed: Concurrently Herewith Docket No.: 42P19260

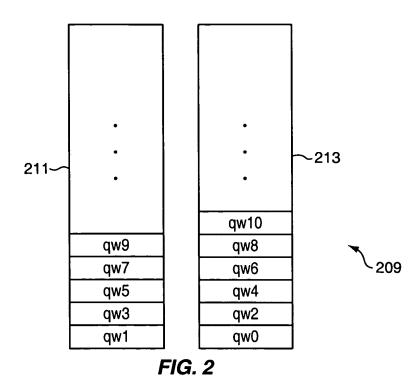
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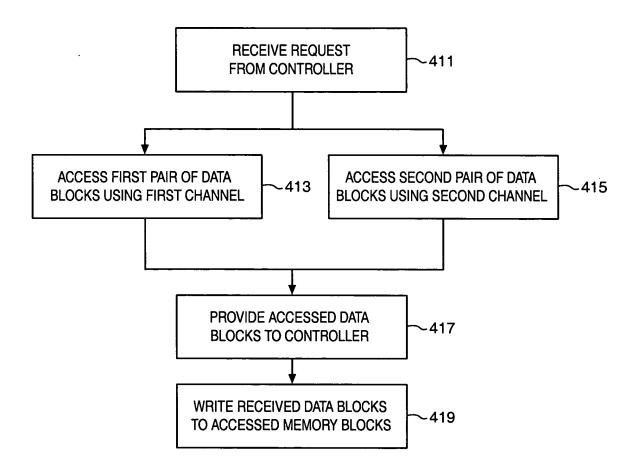


qw32 qw34 -64 byte flip qw31 qw29 qw30 qw28 qw27 qw25 qw26 qw24 ~128 byte flip qw21 qw23 qw20 qw22 -313 qw17 qw19 311~ qw16 qw18 qw13 qw15 qw12 qw14 **∕** 309 qw11 qw9 qw10 8wp ~64 byte flip qw5 qw7 . qw6 qw4 cwp qw1 qw0 qw2

FIG. 3

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FIG. 4



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